

FIG. 1

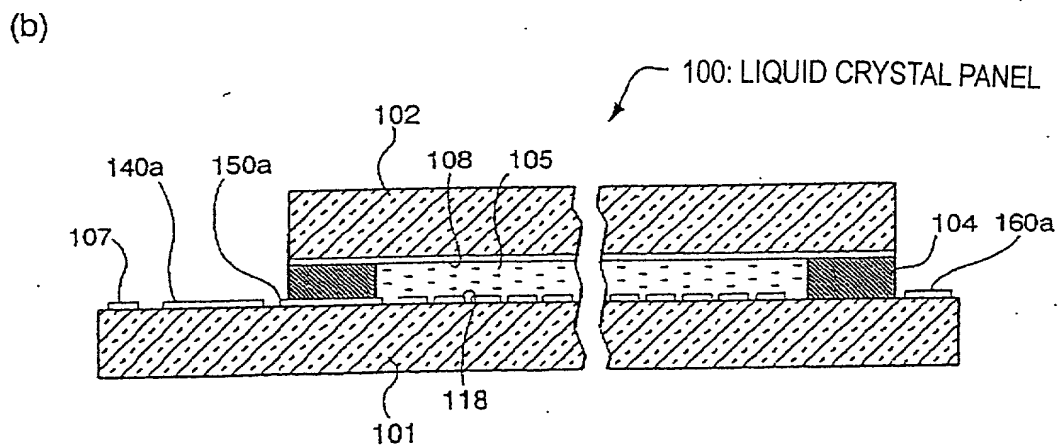
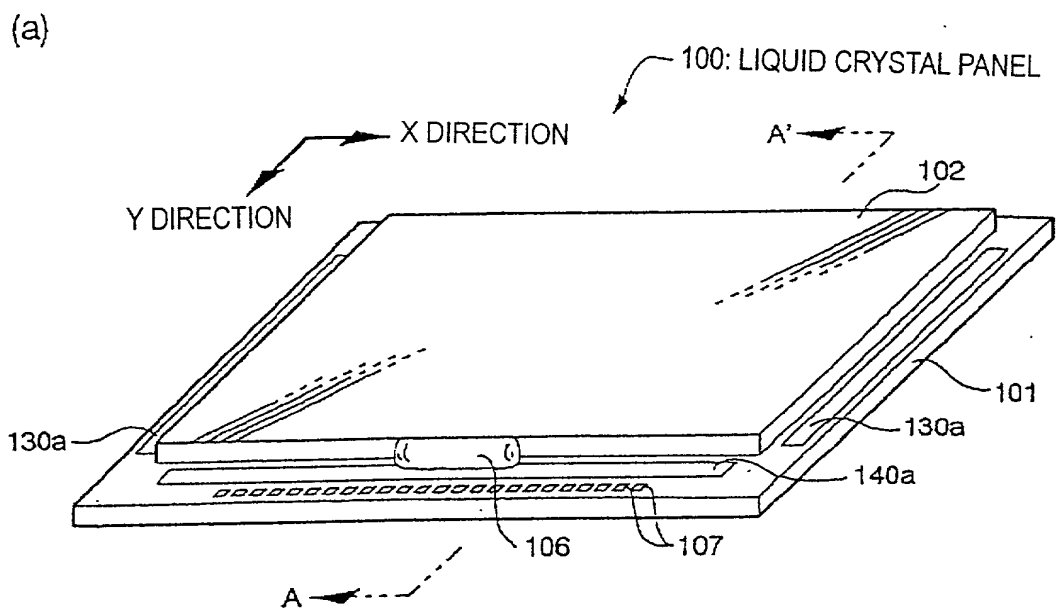


FIG. 2

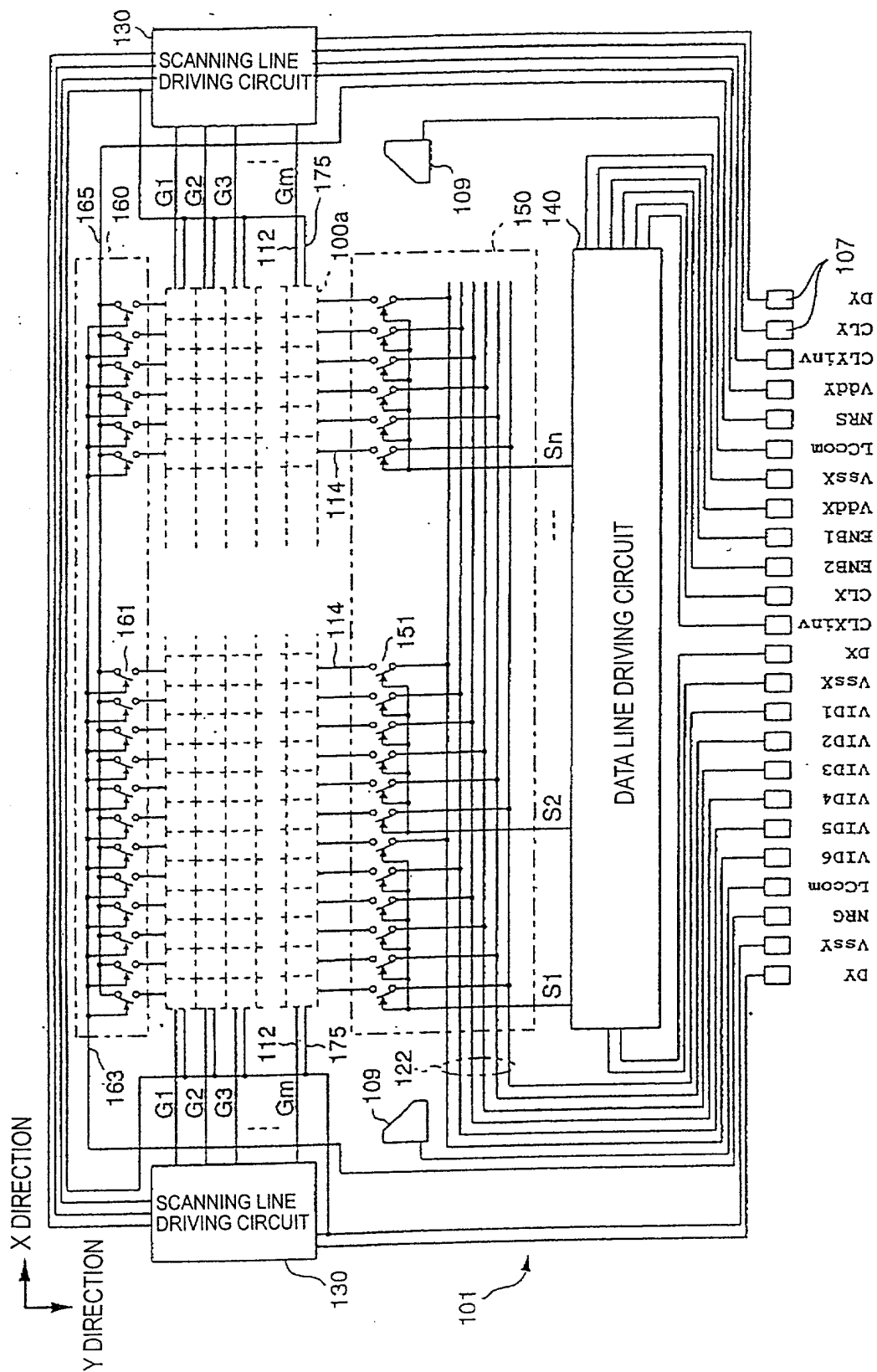
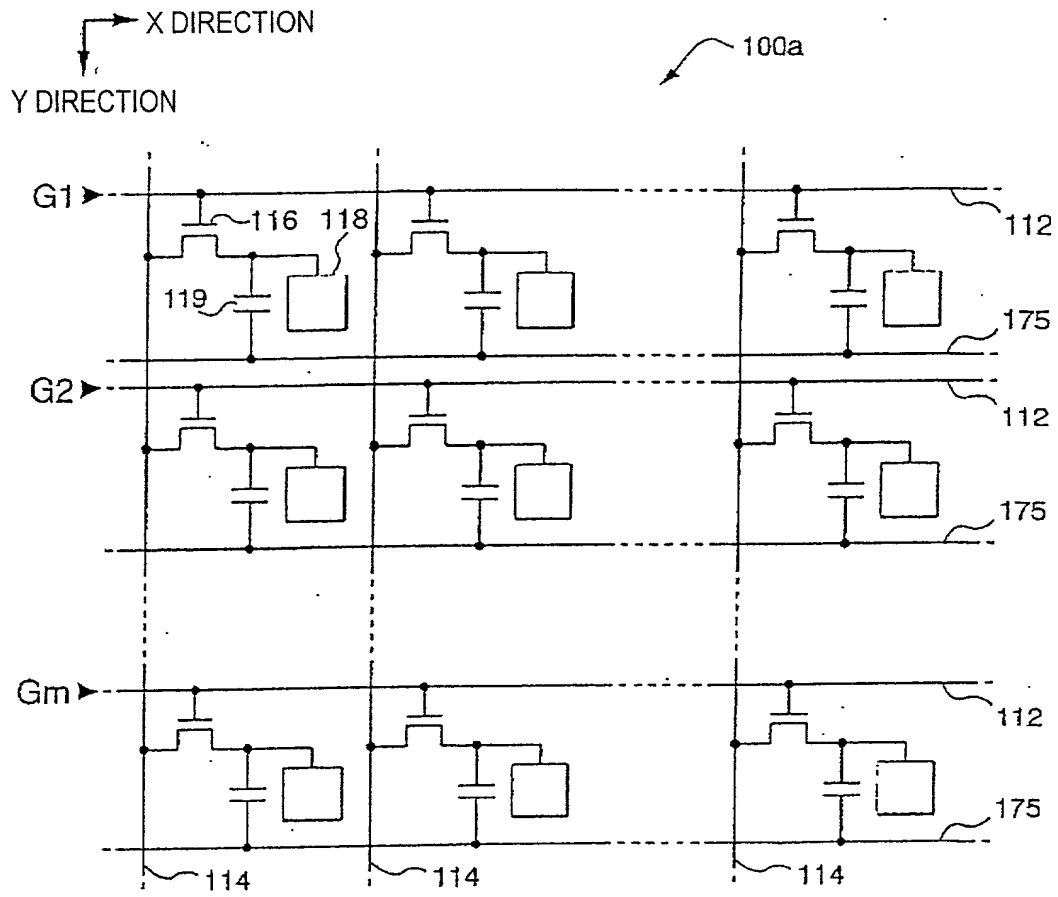


FIG. 3



0958470.051703

FIG. 4

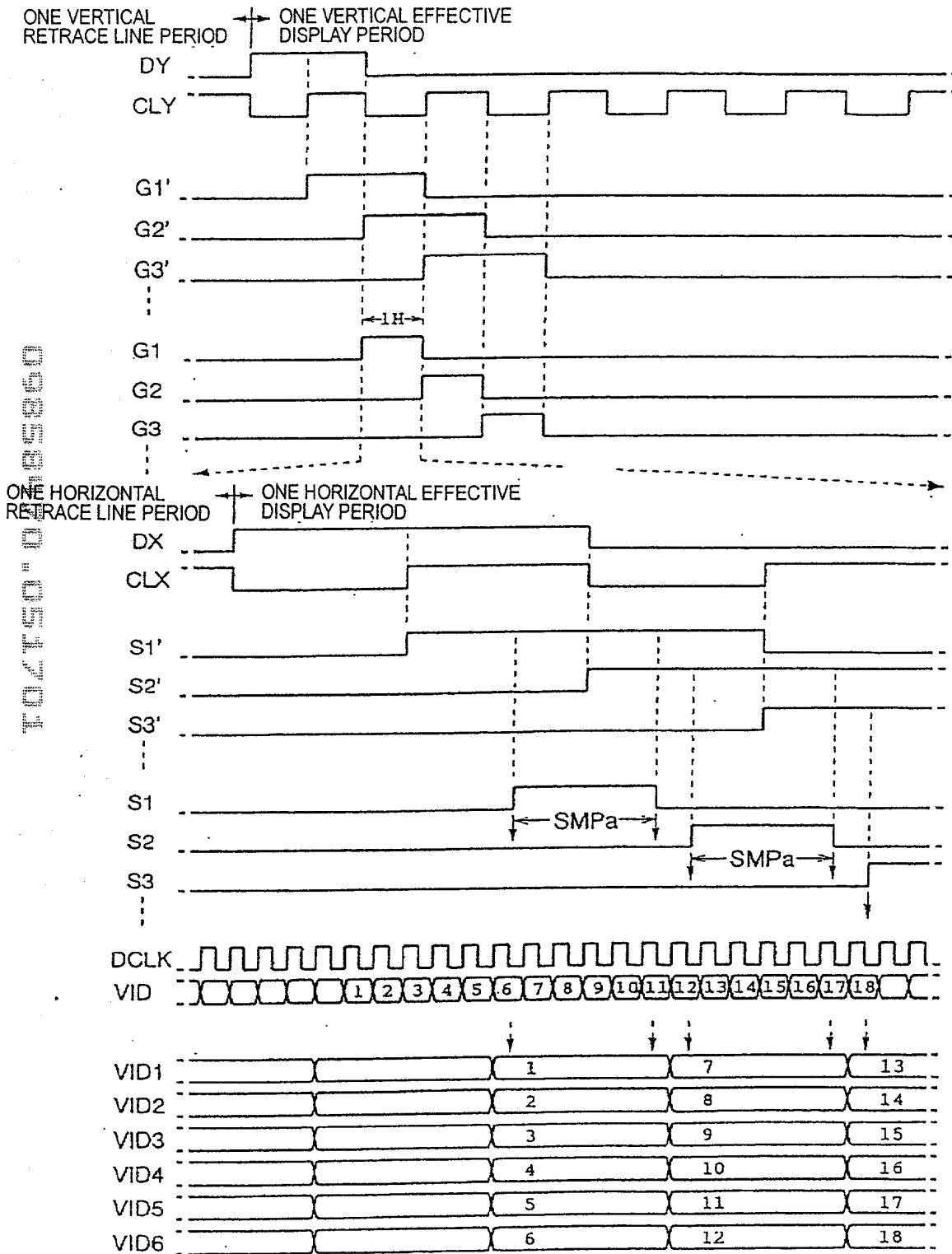
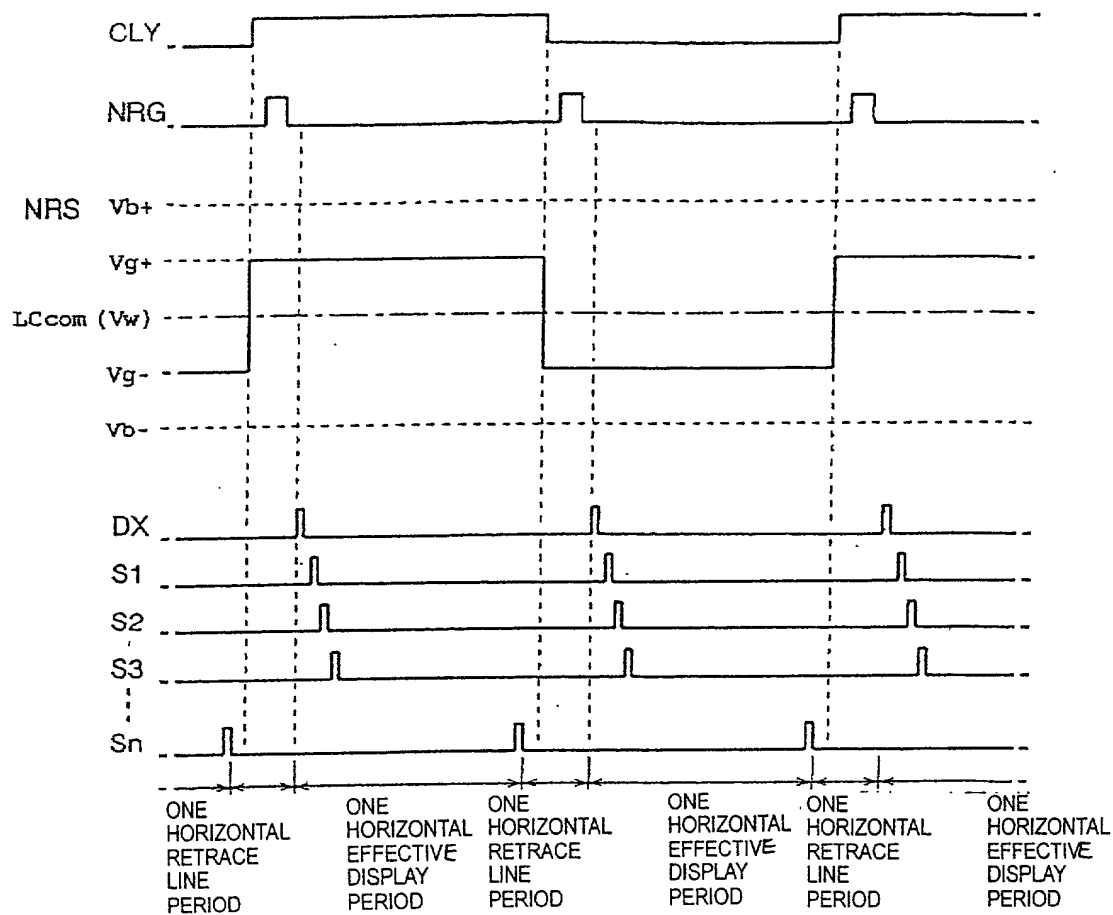
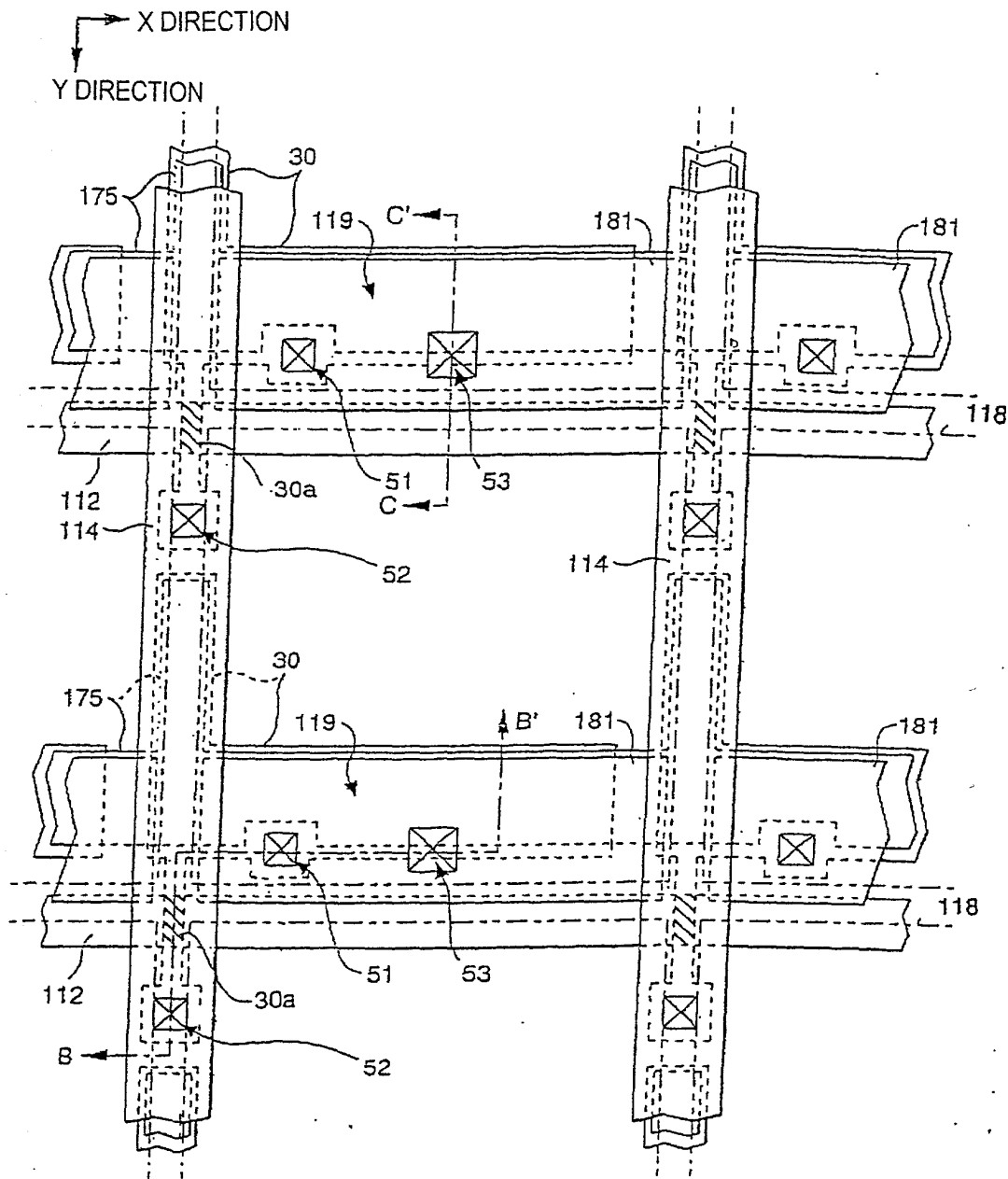


FIG. 5



09658470.051704

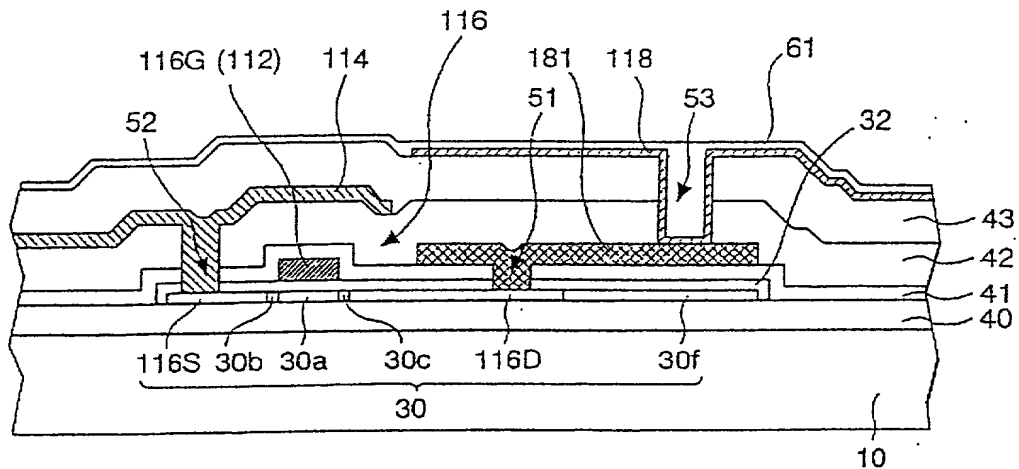
FIG. 6



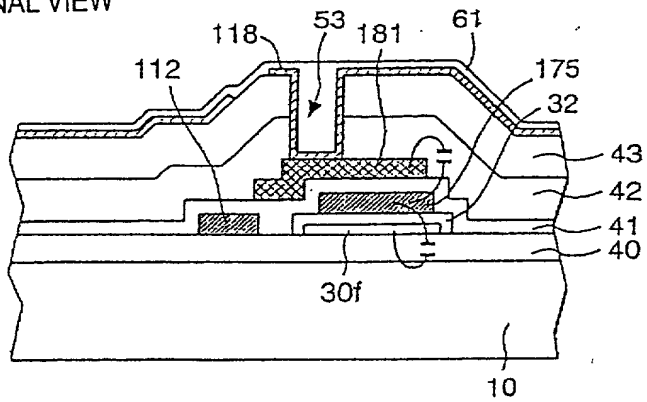
09858470.04185860

[illegible]

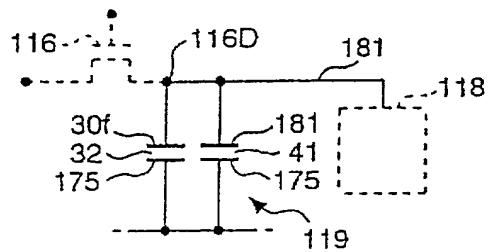
(a) LINE B-B' CROSS-SECTIONAL VIEW



(b) LINE C-C' CROSS-SECTIONAL VIEW



(c) EQUIVALENT CIRCUIT OF STORAGE CAPACITOR



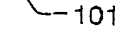
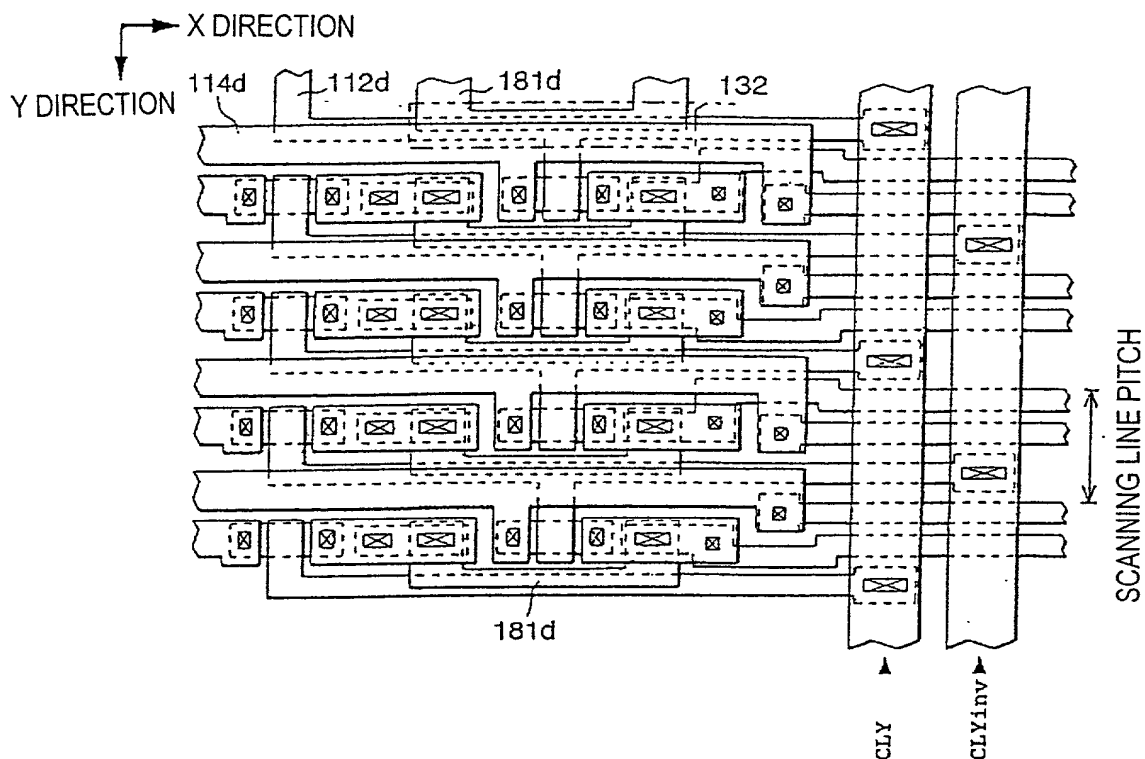
[illegible]



FIG. 9

(a)



(b)

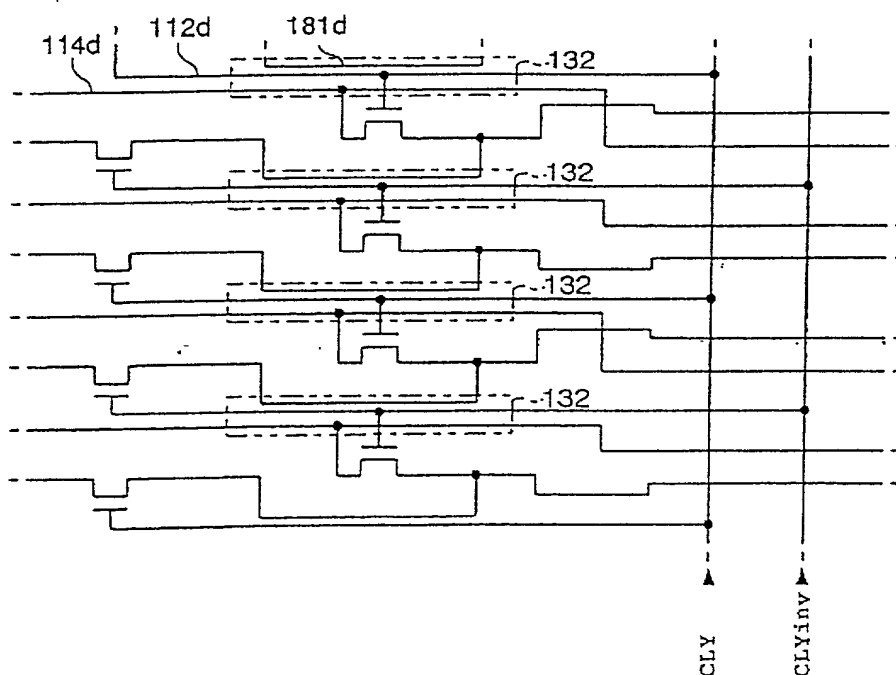
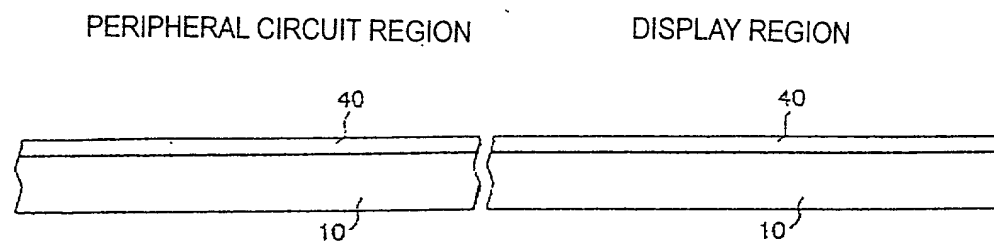
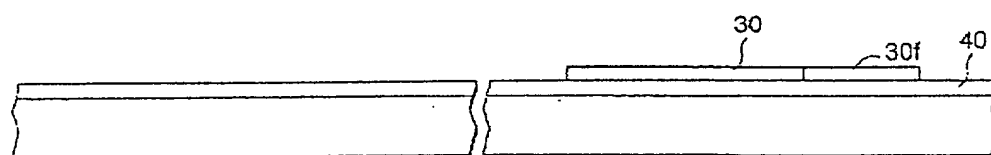


FIG. 10 |

(1)



(2)



(3)



FIG. 11

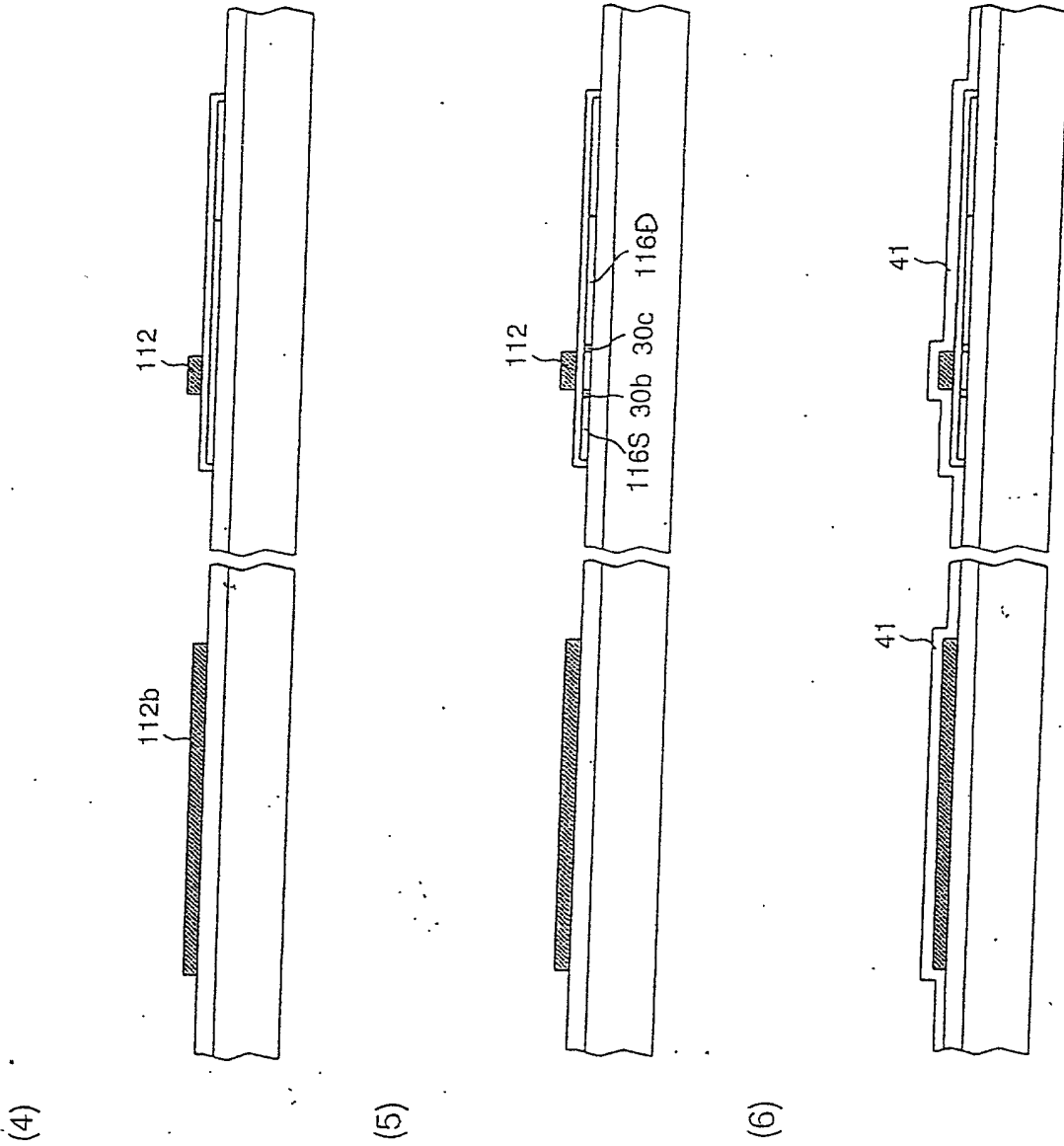
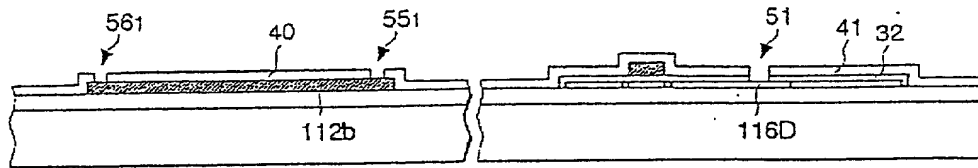
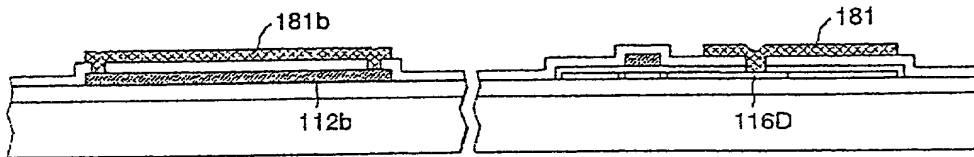


FIG. 12

(7)



(8)



(9)

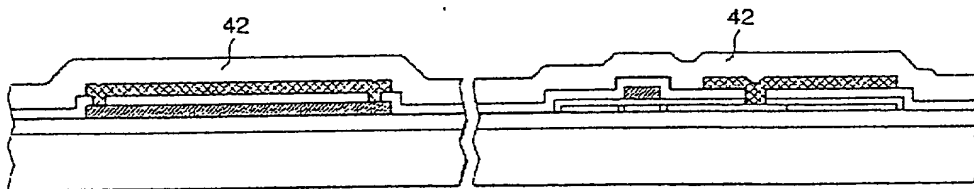
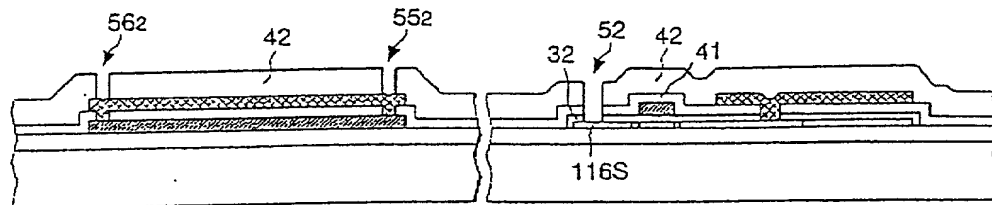
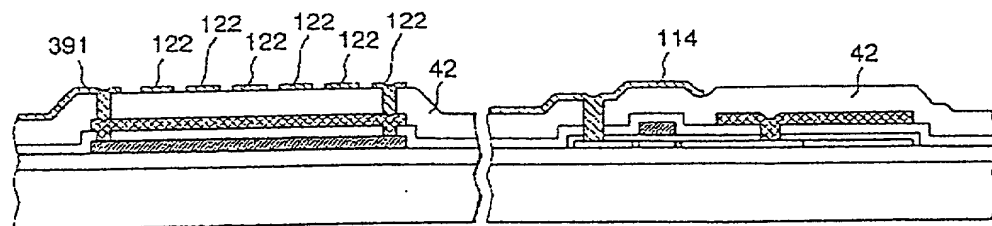


FIG. 13

(10)



(11)



(12)

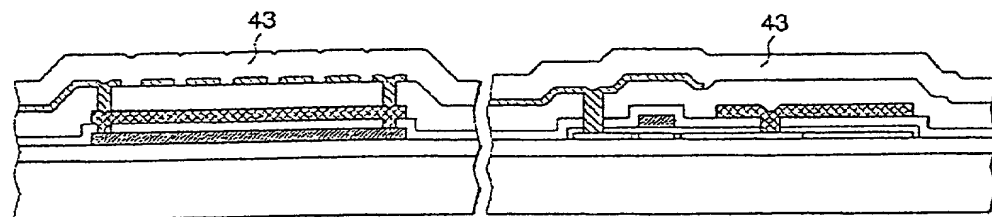
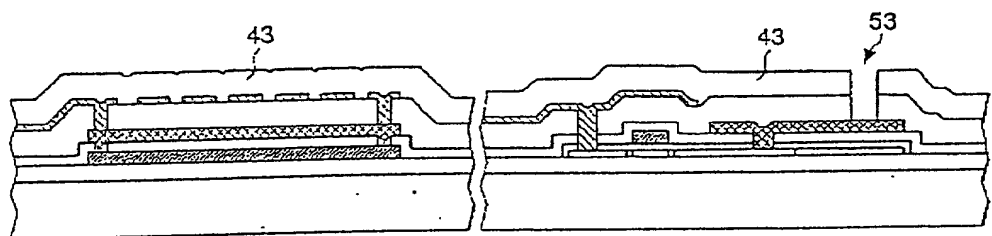


FIG. 14

(13)



(14)

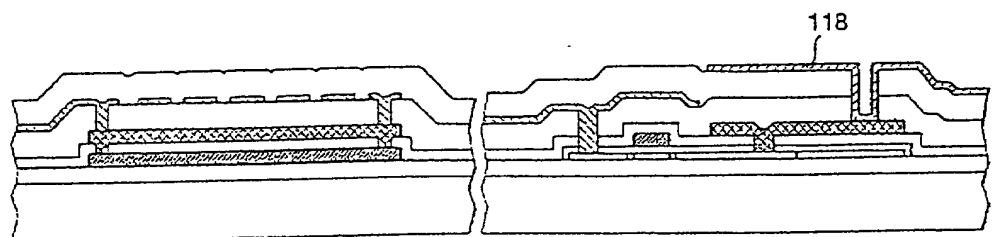
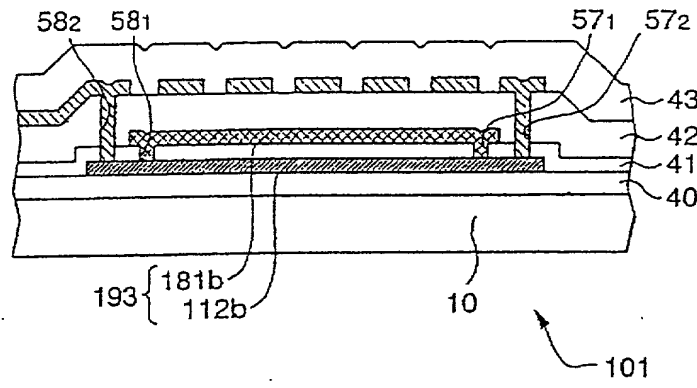


FIG. 15

(a)



(b)

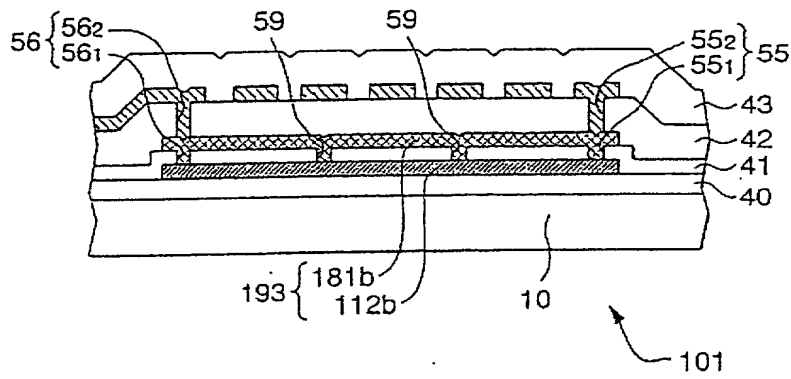


FIG. 16

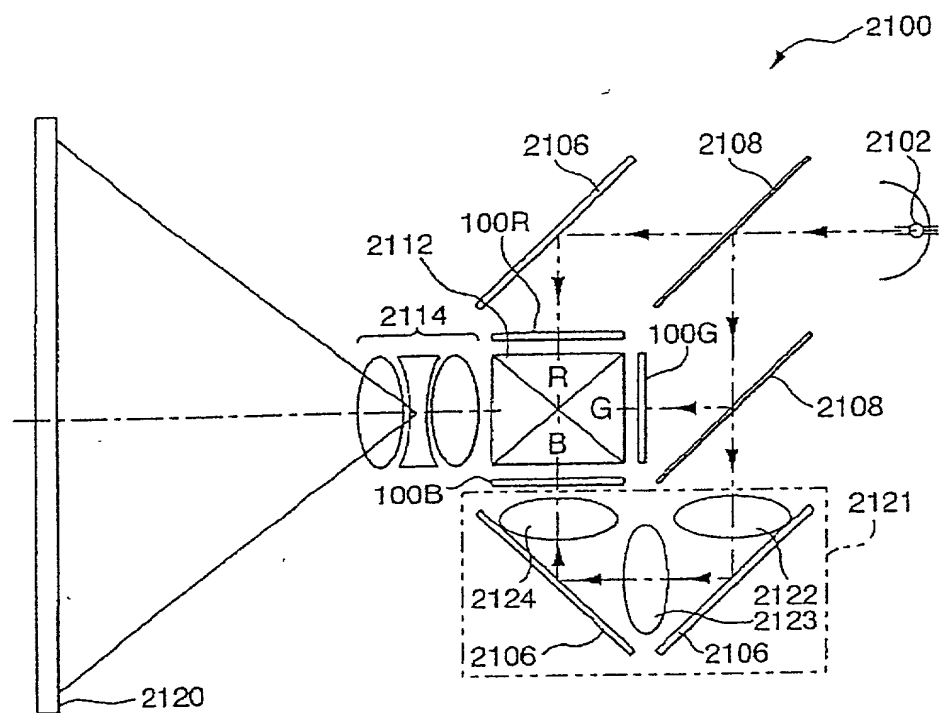


FIG. 17

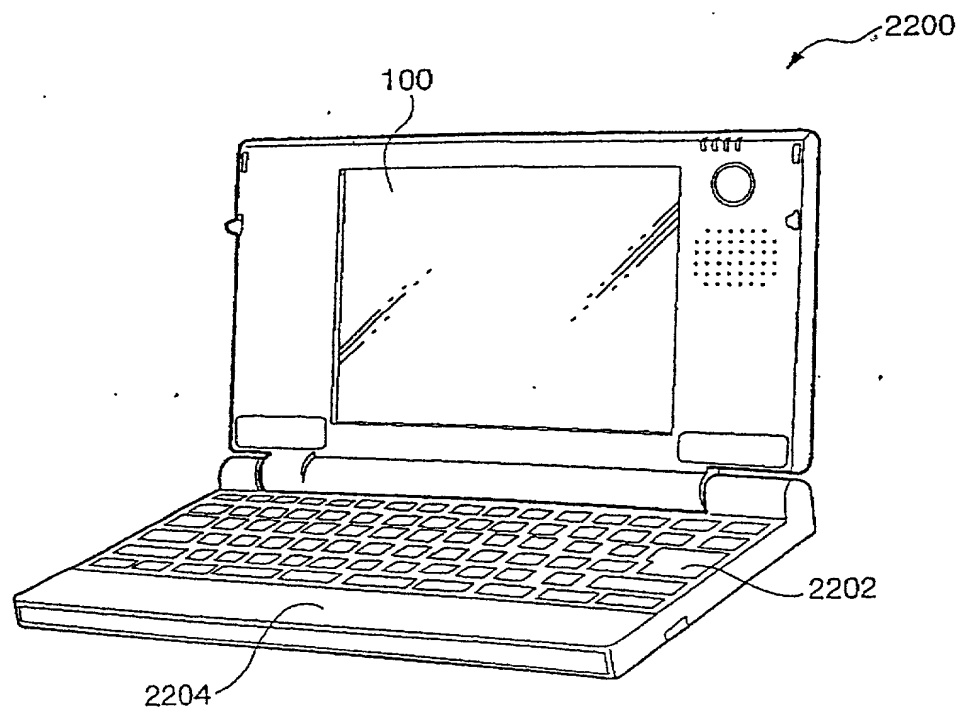


FIG. 18

